

The listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1.-5 (Canceled)

6. (Currently Amended) A method of manufacturing a semiconductor device, comprising:

~~a step of~~ forming, over a substrate, a metal layer, an oxide layer in contact with the metal layer, and a layer to be peeled including a semiconductor element over the oxide layer;

~~a step of~~ oxidizing the metal layer to form a metal oxide layer between the metal layer and the oxide layer;

~~a step of~~ peeling the layer to be peeled that is bonded to the support from the substrate with a physical means within the metal oxide layer, at an interface between the metal oxide layer and the oxide layer, or at an interface between the metal oxide layer and the metal layer after bonding a support and the layer to be peeled.

7. (Currently Amended) A method of manufacturing a semiconductor device, comprising:

~~a step of~~ forming an insulator layer on a substrate, a metal layer in contact with the insulator layer, an oxide layer in contact with the metal layer, and a layer to be peeled including a semiconductor element above the oxide layer;

~~a step of~~ oxidizing the metal layer to form a metal oxide layer between the metal layer and the insulating layer, between the metal layer and the oxide layer, or both between the metal layer and the insulating layer and between the metal layer and the oxide layer;

~~a step of peeling the layer to be peeled that is bonded to the support from the substrate with a physical means within the metal oxide layer in contact with the insulating layer, at an interface between the metal oxide layer in contact with the insulating layer and the insulating layer, at an interface between the metal oxide layer in contact with the insulating layer and the metal layer, within the metal oxide layer in contact with the oxide layer, at an interface between the metal oxide layer in contact with the oxide layer and the oxide layer, or at an interface between the metal oxide layer in contact with the oxide layer and the metal layer after bonding a support and the layer to be peeled.~~

8. (Currently Amended) A method of manufacturing a semiconductor device, comprising:

~~a step of forming, over a substrate, a metal layer, an oxide layer in contact with the metal layer, and a layer to be peeled including a semiconductor element over the oxide layer;~~

~~a step of oxidizing the metal layer to form a metal oxide layer between the metal layer and the oxide layer;~~

~~a step of peeling the layer to be peeled that is bonded to the support from the substrate with a physical means at a portion between the metal layer and the oxide layer.~~

9. (Currently Amended) The method according to ~~any one of claims 6 to 8~~ claim 6, wherein ~~the step of oxidizing the metal film is conducted with irradiation of a laser beam, heat treatment, or compound treatment of irradiation of a laser beam and heat treatment.~~

10. (Original) The method according to claim 9, wherein the laser beam is light emitted from a continuous wave oscillation or pulse oscillation solid laser.

11. (Currently Amended) The method according to ~~any one of claims 6 to 8~~ claim 6, wherein the metal layer is a single layer comprising an element selected ~~[[form]]~~ from Ti, Ta, W, Mo, Cr, Nd, Fe, Ni, Co, Zr, and Zn, or one of an alloy material and a compound material including the element as its main component, or a lamination layer thereof.

12. (Currently Amended) The method according to ~~any one of claims 6 to 8~~ claim 6, wherein the oxide layer in contact with the metal layer is a silicon oxide film formed by sputtering.

13. (Currently Amended) The method according to ~~any one of claims 6 to 8~~ claim 6, wherein the substrate is a glass substrate or a quartz substrate, and the support is a plastic substrate or a plastic base material.

14. (Currently Amended) The method according to ~~any one of claims 6 to 8~~ claim 6, wherein the layer to be peeled includes a thin film transistor, a photoelectric conversion element comprising a PIN junction of silicon, an organic light-emitting element, an element comprising a liquid crystal, a memory element, a thin film diode, or a silicon resistor element.

15. (New) The method according to claim 7, wherein oxidizing the metal film is conducted with irradiation of a laser beam, heat treatment, or compound treatment of irradiation of a laser beam and heat treatment.

16. (New) The method according to claim 15, wherein the laser beam is light emitted from a continuous wave oscillation or pulse oscillation solid laser.

17. (New) The method according to claim 7, wherein the metal layer is a single layer comprising an element selected from Ti, Ta, W, Mo, Cr, Nd, Fe, Ni, Co, Zr, and Zn, or one of an alloy material and a compound material including the element as its main component, or a lamination layer thereof.

18. (New) The method according to claim 7, wherein the oxide layer in contact with the metal layer is a silicon oxide film formed by sputtering.

19. (New) The method according to claim 7, wherein the substrate is a glass substrate or a quartz substrate, and the support is a plastic substrate or a plastic base material.

20. (New) The method according to claim 7, wherein the layer to be peeled includes a thin film transistor, a photoelectric conversion element comprising a PIN junction of silicon, an organic light-emitting element, an element comprising a liquid crystal, a memory element, a thin film diode, or a silicon resistor element.

21. (New) The method according to claim 8, wherein oxidizing the metal film is conducted with irradiation of a laser beam, heat treatment, or compound treatment of irradiation of a laser beam and heat treatment.

22. (New) The method according to claim 21, wherein the laser beam is light emitted from a continuous wave oscillation or pulse oscillation solid laser.

23. (New) The method according to claim 8, wherein the metal layer is a single layer comprising an element selected from Ti, Ta, W, Mo, Cr, Nd, Fe, Ni, Co, Zr, and Zn, or one of an alloy material and a compound material including the element as its main component, or a lamination layer thereof.

24. (New) The method according to claim 8, wherein the oxide layer in contact with the metal layer is a silicon oxide film formed by sputtering.

25. (New) The method according to claim 8, wherein the substrate is a glass substrate or a quartz substrate, and the support is a plastic substrate or a plastic base material.

26. (New) The method according to claim 8, wherein the layer to be peeled includes a thin film transistor, a photoelectric conversion element comprising a PIN junction of silicon, an organic light-emitting element, an element comprising a liquid crystal, a memory element, a thin film diode, or a silicon resistor element.

27. (New) A method of manufacturing a semiconductor device, comprising:
forming a metal layer over a substrate,
forming an oxide layer on the metal layer, wherein a metal oxide layer is formed between the metal layer and the oxide layer,
forming a layer to be peeled including a semiconductor element over the oxide layer;
peeling the layer to be peeled that is bonded to the support from the substrate with a physical means at a portion between the metal layer and the oxide layer.

28. (New) The method according to claim 27, wherein the metal layer is a single layer comprising an element selected from Ti, Ta, W, Mo, Cr, Nd, Fe, Ni, Co, Zr, and Zn, or one of an alloy material and a compound material including the element as its main component, or a lamination layer thereof.

29. (New) The method according to claim 27, wherein the oxide layer in contact with the metal layer is a silicon oxide film formed by sputtering.

30. (New) The method according to claim 27, wherein the substrate is a glass substrate or a quartz substrate, and the support is a plastic substrate or a plastic base material.

31. (New) The method according to claim 27, wherein the layer to be peeled includes a thin film transistor, a photoelectric conversion element comprising a PIN junction of silicon, an organic light-emitting element, an element comprising a liquid crystal, a memory element, a thin film diode, or a silicon resistor element.

32. (New) The method according to claim 27, wherein the metal oxide layer is formed after forming the oxide layer.